UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS P.O. Box 1450

P O Box 1450 Alexandria, Virgima 22313-1450 www.uspto.gov

# NOTICE OF ALLOWANCE AND FEE(S) DUE

22879 7590 11/04/2008 HEWLETT PACKARD COMPANY P O BOX 272400, 3404 E. HARMONY ROAD INTELL ECTITAL PROPERTY ADMINISTRATION

FORT COLLINS, CO 80527-2400

EXAMINER

TABONE JR, JOHN J

ART UNIT PAPER NUMBER

2117

DATE MAILED: 11/04/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/635,369	08/06/2003	Richard W. Adkisson	200208998-1	1303

TITLE OF INVENTION: INCREMENT/DECREMENT CIRCUIT FOR PERFORMANCE COUNTER

 APPLN TYPE
 SMALL ENTITY
 ISSUE FEE DUE
 PUBLICATION FEE DUE
 PREV. PAID ISSUE FEE
 TOTAL FEES) DUE
 DATE DUE

 nonprovisional
 NO
 \$1510
 \$300
 \$0
 \$1810
 02/04/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THE APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

#### HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FFE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

### PART B - FEE(S) TRANSMITTAL

# Complete and send this form, together with applicable fee(s), to: Mail Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or Fax (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where

appropriate. All further indicated unless corrects maintenance fee notifica	correspondence includir ed below or directed oth tions	ng the Patent, advance on herwise in Block 1, by (a	rders and notification of r a) specifying a new corres	naintenance fees w pondence address;	ill be and/o	mailed to the current r (b) indicating a sepa	correspondence addre trate "FEE ADDRESS	ss as
	ENCE ADDRESS (Note: Use Bi	ock 1 for any change of address)	Dane	rs. Each additional	paper	g can only be used for ficate cannot be used for such as an assignmental iling or transmission.	or domestic mailings of for any other accompar nt or formal drawing,	f the ying must
P O BOX 27240 INTELLECTUA	7590 11/04 ACKARD COMPA 00, 3404 E. HARMO AL PROPERT 2400	ANY ONY ROAD		Cer	tificate	of Mailing or Trans	mission g deposited with the U st class mail in an enve above, or being facs ate indicated below.	nited dope imile
FORT COLLIN	S, CO 80527-2400						(Depositor's r	ame)
			<u> </u>				(Sign	_
							(	Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTO	RNEY DOCKET NO.	CONFIRMATION NO	l.
10/635,369	08/06/2003		Richard W. Adkisson			200208998-1	1303	
			PERFORMANCE COUNT			L	DATE DUE	
APPLN, TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE	SPEE	TOTAL FEE(S) DUE		
nonprovisional	NO	\$1510	\$300	\$0		\$1810	02/04/2009	
EXAM		ART UNIT	CLASS-SUBCLASS					
TABONE J		2117	714-724000  2. For printing on the p					
"Fee Address" ind PTO/SB/47; Rev 03-0 Number is required.	ondence address (or Cha B/122) attached. ication (or "Fee Address 32 or more recent) attach	nge of Correspondence  "Indication form ed. Use of a Customer	(I) the names of up to or agents OR, alternative (2) the name of a single registered attorney or a 2 registered patent atto- listed, no name will be	ely, e firm (having as a gent) and the name meys or agents. If a printed.	memb	era 2		
PLEASE NOTE: Uni recordation as set fort (A) NAME OF ASSI	less an assignee is ident h in 37 CFR 3.11. Comp GNEE	ified below, no assignee pletion of this form is NO	THE PATENT (print or typ data will appear on the p T a substitute for filing an (B) RESIDENCE: (CITY crinted on the patent):	atent. If an assign assignment. and STATE OR C	OUNT	TRY)		
4a. The following fee(s)	are submitted:	41	b. Payment of Fee(s): (Plea A check is enclosed. Payment by credit car The Director is hereby overpayment, to Depo	se first reapply ar	is atta	viously paid issue fee sched.	shown above)	
	s SMALL ENTITY state	is. See 37 CFR 1.27.	☐ b. Applicant is no lon					
NOTE: The Issue Fee an interest as shown by the	d Publication Fee (if req records of the United Sta	uired) will not be accepte tes Patent and Trademark	d from anyone other than t Office.	he applicant; a regi	stered.	attorney or agent; or th	ne assignee or other par	ty in
Authorized Signature				Date				
Typed or printed name			Registration No.					
This collection of inform an application. Confiden submitting the complete this form and/or suggesti Box 1450, Alexandria, V Alexandria, Virginia 223	nation is required by 37 C tiality is governed by 35 d application form to the ions for reducing this but (irginia 22313-1450. DC k13-1450.	FR 1.311. The informatic U.S.C. 122 and 37 CFR USPTO. Time will vary rden, should be sent to the ONOT SEND FEES OR	on is required to obtain or r 1.14. This collection is est depending upon the indiv e Chief Information Office COMPLETED FORMS TO	etain a benefit by the imated to take 12 r idual case. Any co r, U.S. Patent and D'THIS ADDRESS	ne pub ninute mmen Trader SEN	lic which is to file (and is to complete, including its on the amount of the nark Office, U.S. Dep D TO: Commissioner	I by the USPTO to pro ig gathering, preparing me you require to com artment of Commerce, for Patents, P.O. Box 1	cess) , and plete P.O. 450,

PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010. OMB 0651-0033

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.



FORT COLLINS, CO 80527-2400

# UNITED STATES PATENT AND TRADEMARK OFFICE

#### UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS

P O Box 1450 Alexandria, Virgima 22313-1450 www.uspto.gov

DATE MAILED: 11/04/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/635,369	08/06/2003	Richard W. Adkisson	200208998-1	1303	
22879	7590 11/04/2008		EXAMINER		
HEWLETT PACKARD COMPANY			TABONE JR, JOHN J		
P O BOX 272400, 3404 E. HARMONY ROAD			ART UNIT	PAPER NUMBER	
INTELLECTUAL PROPERTY ADMINISTRATION			2117		

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 1065 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 1055 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

# Application No. Applicant(s) 10/635,369 ADKISSON, RICHARD W. Notice of Allowability Examiner Art Unit JOHN J. TARONE JR. -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. This communication is responsive to amendment and arguments filed 08/22/2008. The allowed claim(s) is/are 1-11 and 18-20; renumbered to 1-14. 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) $\square$ All b) ☐ Some\* c) ☐ None of the: 1. T Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). \* Certified copies not received: \_\_\_\_\_. Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. (a) Including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d). 6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. Attachment(s) 1. | Notice of References Cited (PTO-892) 5. Notice of Informal Patent Application 2. Notice of Draftperson's Patent Drawing Review (PTO-948) Interview Summary (PTO-413), Paper No./Mail Date Information Disclosure Statements (PTO/SB/08). 7. X Examiner's Amendment/Comment Paper No./Mail Date 4. T Examiner's Comment Regarding Requirement for Deposit 8. X Examiner's Statement of Reasons for Allowance of Biological Material

/John J. Tabone, Jr./

Examiner, Art Unit 2117 10/16/2008

Other .

/JACQUES H LOUIS-JACQUES/

Supervisory Patent Examiner, Art Unit 2100

Application/Control Number: 10/635,369 Page 2

Art Unit: 2117

## DETAILED ACTION

Claims 1-11 and 18-20 are currently pending and have been examined. Claims
 6.7. 18 and 19 have been amended. Claims 12-17 have been cancelled.

## Response to Arguments

Applicant's arguments filed 08/22/2008 with respect to claims 1-11 and 18-20
have been fully considered and are persuasive. The Non-Final Rejection of 01/14/2008
has been withdrawn

## EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

# Specification, paragraph [0002]:

In the line(s) reciting "Serial No. 10/635,083, filed August 6, 2003, entitled GENERAL PURPOSE PERFORMANCE COUNTER" add ", now US Patent No. 7.424.397." after the word "COUNTER".

Art Unit: 2117

In the line(s) reciting "Serial No. No. 10/635,371, filed August 6, 2003, entitled COVERAGE CIRCUIT FOR PERFORMANCE COUNTER" add ", now US Patent No. 7.415.643." after the word "COUNTER".

In the line(s) reciting "Serial No. No. 10/635,372, filed August 6, 2003, entitled COVERAGE DECODER CIRCUIT FOR PERFORMANCE COUNTER" add ", now US Patent No. 7,275,191," after the word "COUNTER".

In the line(s) reciting "Serial No. 10/635,103, filed August 6, 2003, entitled DATA SELECTION CIRCUIT FOR PERFORMANCE COUNTER" add ", now US Patent No. 7,404,112," after the word "COUNTER".

In the line(s) reciting "Serial No. 10/635,079, filed August 6, 2003, entitled ZEROING CIRCUIT FOR PERFORMANCE COUNTER" add ", now US Patent No. 7.430.696," after the word "COUNTER".

In the line(s) reciting "Serial No. 10/635,373, filed August 6, 2003, entitled MATCH CIRCUIT FOR PERFORMANCE COUNTER" add ", now US Patent No. 7,331,003," after the word "COUNTER".

# Specification, paragraph [0003]:

In the line(s) reciting "10/402,092" add ", now US Patent No. 7,146,538," after "10/402,092".

In the line(s) reciting "10/402,034" add ", now US Patent No. 7,188,277," after "10/402,034".

### Specification, paragraph [0024]:

Art Unit: 2117

In the line(s) reciting "Serial No. 10/635,373, filed August 6, 2003, entitled MATCH CIRCUIT FOR PERFORMANCE COUNTER" add ", now US Patent No. 7, 331,003," after the word "COUNTER".

## Specification, paragraph [0026]:

In the line(s) reciting "Serial No. 10/635,103, filed August 6, 2003, entitled DATA SELECTION CIRCUIT FOR PERFORMANCE COUNTER" add ", now US Patent No. 7.404.112." after the word "COUNTER".

In the line(s) reciting "Serial No. 10/635,079, filed August 6, 2003, entitled ZEROING CIRCUIT FOR PERFORMANCE COUNTER" add ", now US Patent No. 7.430.696," after the word "COUNTER".

# Allowable Subject Matter

Claims 1-11 and 18-20 are allowed.

The following is an Examiner's Statement of Reasons for Allowance:

The present invention relates to an increment/decrement circuit for use with a general purpose performance counter ("GPPC") connected to a bus carrying debug

The claimed invention as set forth in claim 1 (broadest claim) recites features such as:

An increment/decrement circuit for use with a general purpose performance counter ("GPPC") connected to a bus carrying debug data, the increment/decrement circuit comprising:

Art Unit: 2117

a delay circuit block operable to receive and align a multi-bit block of said debug data;

a first mask circuit connected to said delay circuit block, wherein said first mask circuit is operable to select a first portion of said aligned, multi-bit block of debug data:

a second mask circuit connected to said delay circuit block, wherein said second mask circuit is operable to select a second portion of said aligned, multi-bit block of debug data;

an accumulation circuit connected to said first mask circuit and said second mask circuit, said accumulation circuit for generating an accumulated value using said first portion and said second portion.

The prior arts of record, namely **Elliott et al.** (US-6826247, teach an increment/decrement circuit which is part of a digital phase lock loop and high precision digital phase comparator circuit. The prior arts of record also teach a reference clock signal 504 can be delayed using delay chain 502. Further, the delay measurement and ambiguity resolver circuit 512 of prior arts of record receives results from phase detectors 505 and based on the information from the phase detection, generates an increment or decrement signal to averaging counter 514 based on whether the measured delay of delay chain 502 is too fast or too slow.

The prior arts of record, however, fail to teach, singly or in combination, a delay circuit block operable to receive and align a multi-bit block of said debug data; a first mask circuit connected to said delay circuit block, wherein said first mask circuit is operable to select a first portion of said aligned, multi-bit block of debug data; a second

Art Unit: 2117

mask circuit connected to said delay circuit block, wherein said second mask circuit is operable to select a second portion of said aligned, multi-bit block of debug data; an accumulation circuit connected to said first mask circuit and said second mask circuit, said accumulation circuit for generating an accumulated value using said first portion and said second portion. As such, modification of the prior art of record to include the claimed delay circuit block, first and second mask circuit and accumulation circuit can only be motivated by hindsight reasoning, or by changing the intended use and function of the prior art themselves. Therefore, it is not clear that one of ordinary skill in the art at the time of the invention would have made the necessary modifications to the prior art of record to encompass the delay circuit block, first and second mask circuit and accumulation circuit set forth in the present application. Moreover, none of the prior arts of record, taken either alone or in combination, anticipate nor render obvious the delay circuit block, first and second mask circuit and accumulation circuit as set forth in claim 1. Independent claim 18 recites similar patentable features and is allowed for the same reasons as claim 1. Hence, claims 1-11 and 18-20 are allowable over the prior arts of record.

The Examiner agrees with the Applicant's arguments with regard to this feature in view of the arts of record; therefore, the Examiner favors the allowance of claims 1-11 and 18-20. Any comments considered necessary by applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance"

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JOHN J. TABONE JR whose telephone number is (571)272-3827. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, JACQUES H. LOUIS JACQUES can be reached on (571) 272-6962. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/JACQUES H LOUIS-JACQUES/ Supervisory Patent Examiner, Art Unit 2100 /John J. Tabone, Jr./ Examiner Art Unit 2117 10/16/2008